Examiner: Landau, Matthew C.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:			
Ji Yong PARK et al.			
Application No.: 10/734,162	Group Art Unit: 2815		

For: THIN FILM TRANSISTOR WITH LDD/OFFSET STRUCTURE

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

Filed: December 15, 2003

Sir:

In accordance with the duty of disclosure provisions of 37 CFR § 1.56, there is hereby provided certain information which the Examiner may consider material to the examination of the subject U.S. patent application. It is requested that the Examiner make this information of record if it is deemed material to the examination of the subject application.

1. Enclosures accompanying this Information Disclosure Statement are:

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1b.		Copy(ies) of IDS citation(s), except for U.S. Patents and U.S. Patent
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1c.		English language copy of a communication(s) from a foreign Patent Office
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1d.	П	English language translation (Abstract Only) attached to non-English
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1e.	П	Explanations of Relevancy of References (ATTACHMENT 1(e), hereto) for
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- 2. This Information Disclosure Statement is filed under 37 CFR § 1.97(i) and/or 37 C.F.R. §1.501 to be placed at least in the file.
- 3. No admission is made that the information cited in this Statement is, or is considered to be, material to patentability nor a representation that a search has been made (other than search report(s) from a counterpart foreign application or a PCT International Search Report, if submitted herewith). 37 CFR §§ 1.97(g) and (h).

Serial No.: 10/734,162

4. The Commissioner is authorized to credit any overpayment or charge any additional fee required under 37 CFR § 1.17 for this Information Disclosure Statement to Deposit Account No. 503333.

Respectfully submitted,

STEIN, MCEWEN & BUI, LLP

Dated: 3 /14 / 08

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